



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: R. Daniel McGrath et al. PATENT APPLICATION
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For: DUAL-MODE CMOS INTEGRATED IMAGER

Supplemental Information Disclosure Statement
with Certification under 37 CFR § 1.97(e)(1)

Hon. Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure under 37 CFR § 1.97(e). A copy of the reference is enclosed.

FOREIGN PATENT DOCUMENT

<u>Publication No.</u>	<u>Published Date</u>	<u>Country</u>
0 942 592 A2	September 15, 1999	EPO

The undersigned hereby certifies that the item of information contained in this Supplemental Information Disclosure Statement was cited in a communication received from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231

Signed: Brenda Elmidolan
Typed Name: Brenda Elmidolan

Date: October 10, 2001

Respectfully submitted,

Thomas Schneck

Thomas Schneck
Reg. No. 24,518

P.O. Box 2-E
San Jose, CA 95109-0005
(408) 297-9733